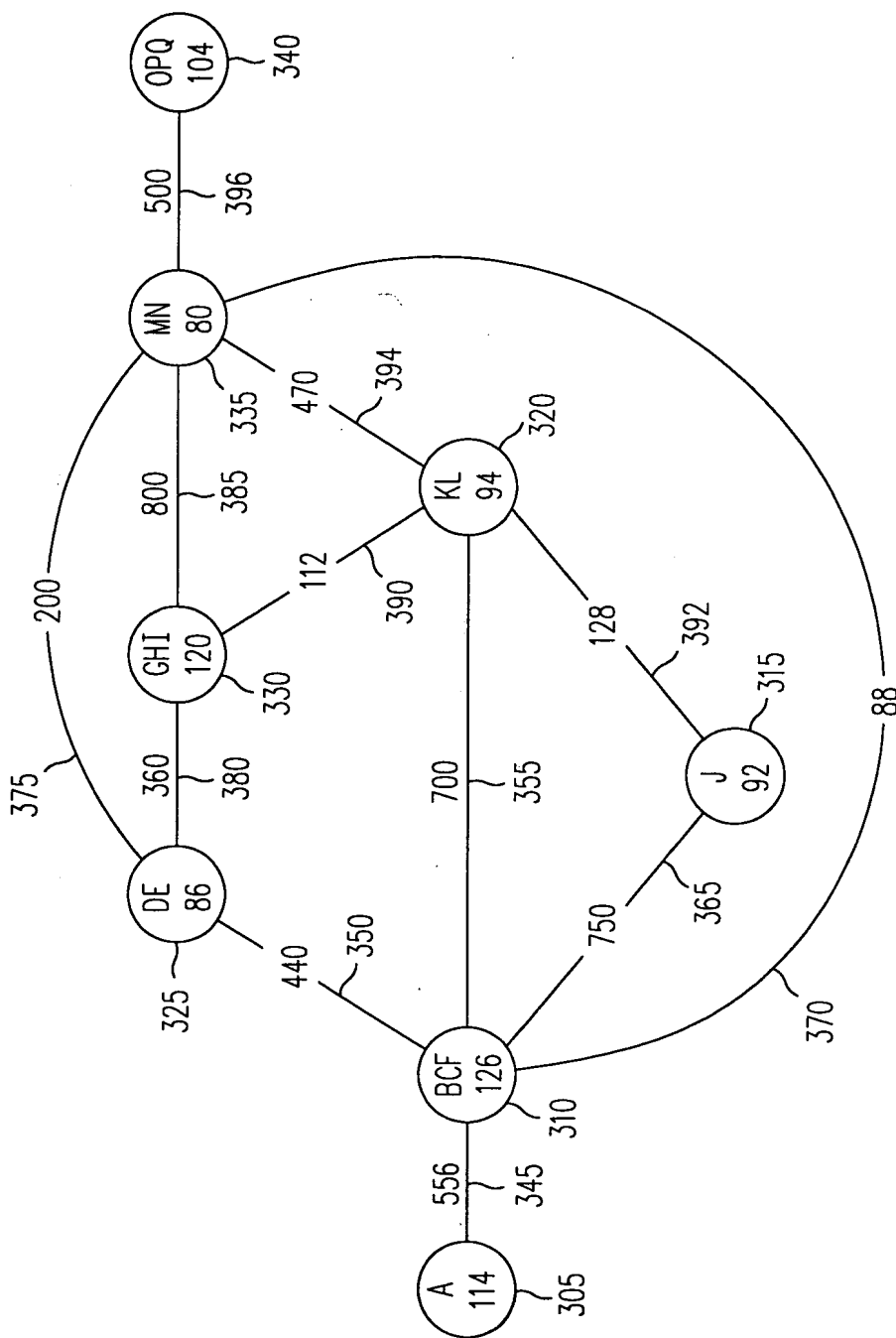


G<sub>1</sub> Cache line = 128 bytes = n<sub>1</sub>

FIG. 1





$G_2 n_2 = 512$  bytes (not realistic)

**FIG. 3**

$G_2 n_2 = 512$  bytes (not realistic)

**FIG. 4**

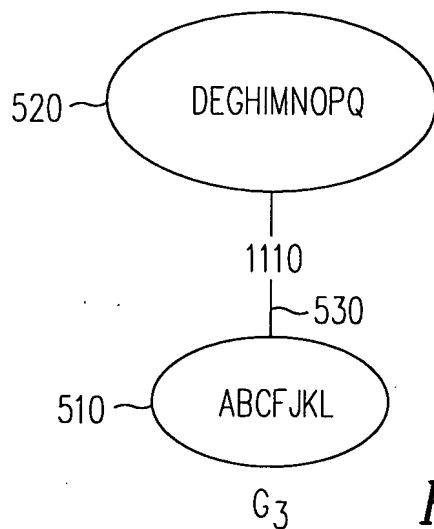


FIG. 5

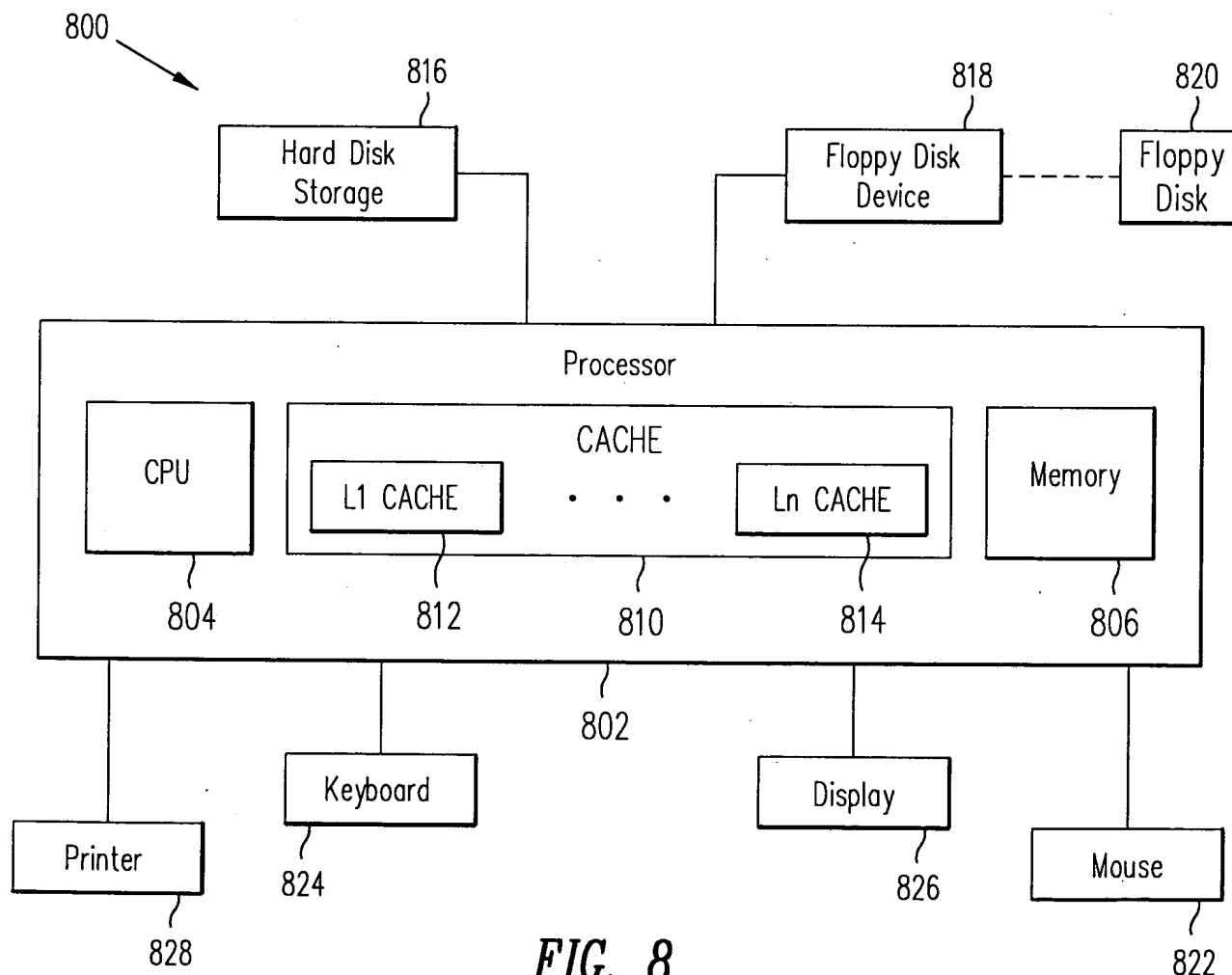


FIG. 8

Determine memory hierarchy parameters

Profile the target program  
(Identify basic blocks, control flow, & transition frequencies)

Construct Program Execution Graph,  
a weighted undirected graph comprising nodes  
representing basic blocks & edges representing  
transfer of control between pairs of basic blocks.  
Weight of a node is size of represented basic block.  
Weight of an edge is frequency of transition  
between the pair of basic blocks it connects.

FIG. 6a

FIG. 6b

Key to  
**FIG. 6**

$i = 1$   
 $N_1 = \text{smallest power of 2 multiple of}$   
 $\text{cache line size} > \text{all basic block sizes}$  — 630

Partition PEG such that  
all clusters have size  $\leq N_1$  ~ 6.35

Pad clusters with unexecuted nodes & if any unexecuted nodes cannot be included in existing clusters, then create new clusters of only unexecuted nodes using bin packing — 640

645 Create next level PEG from  
level 1 partitioned PEG

**FIG. 6a**

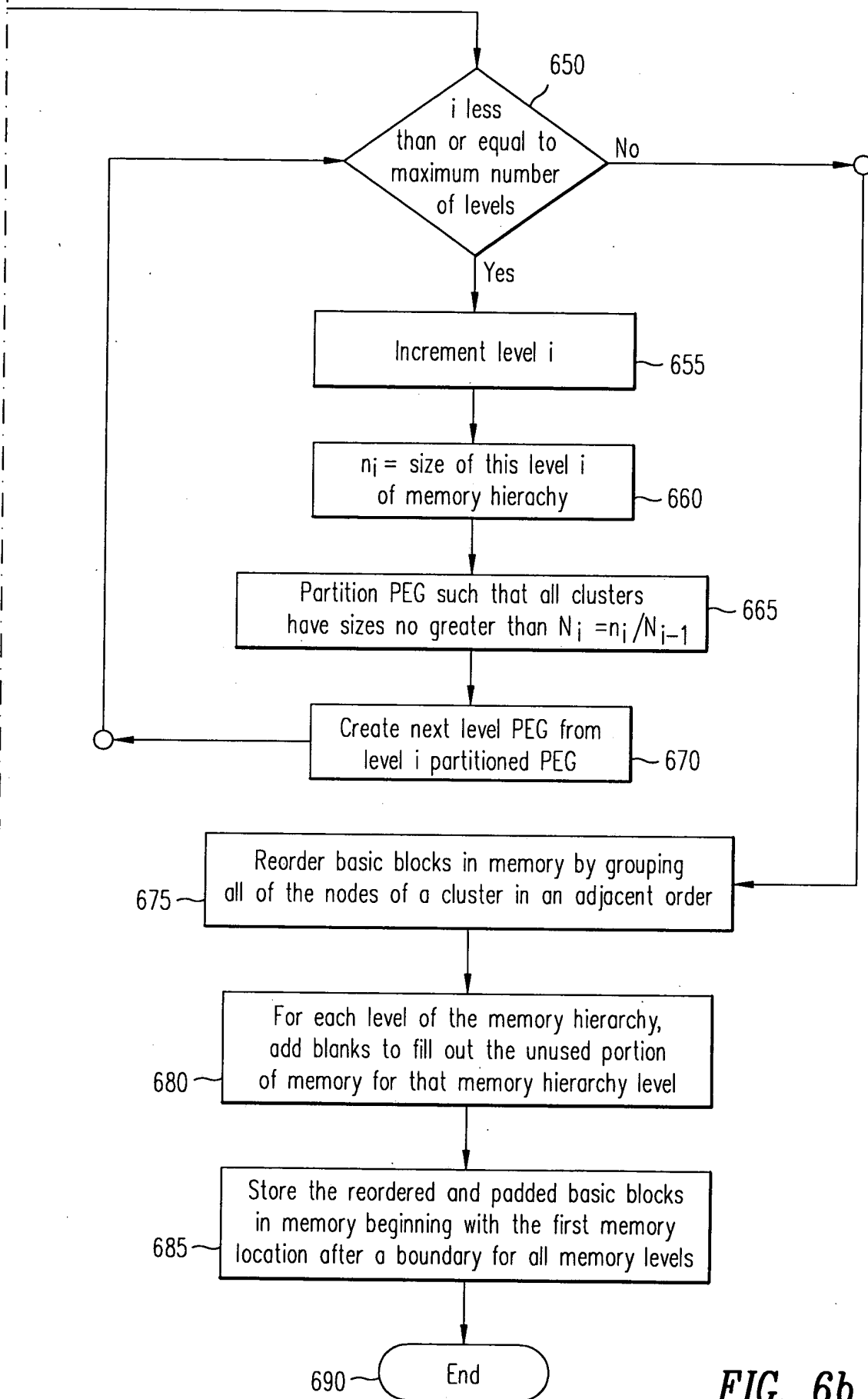


FIG. 6b

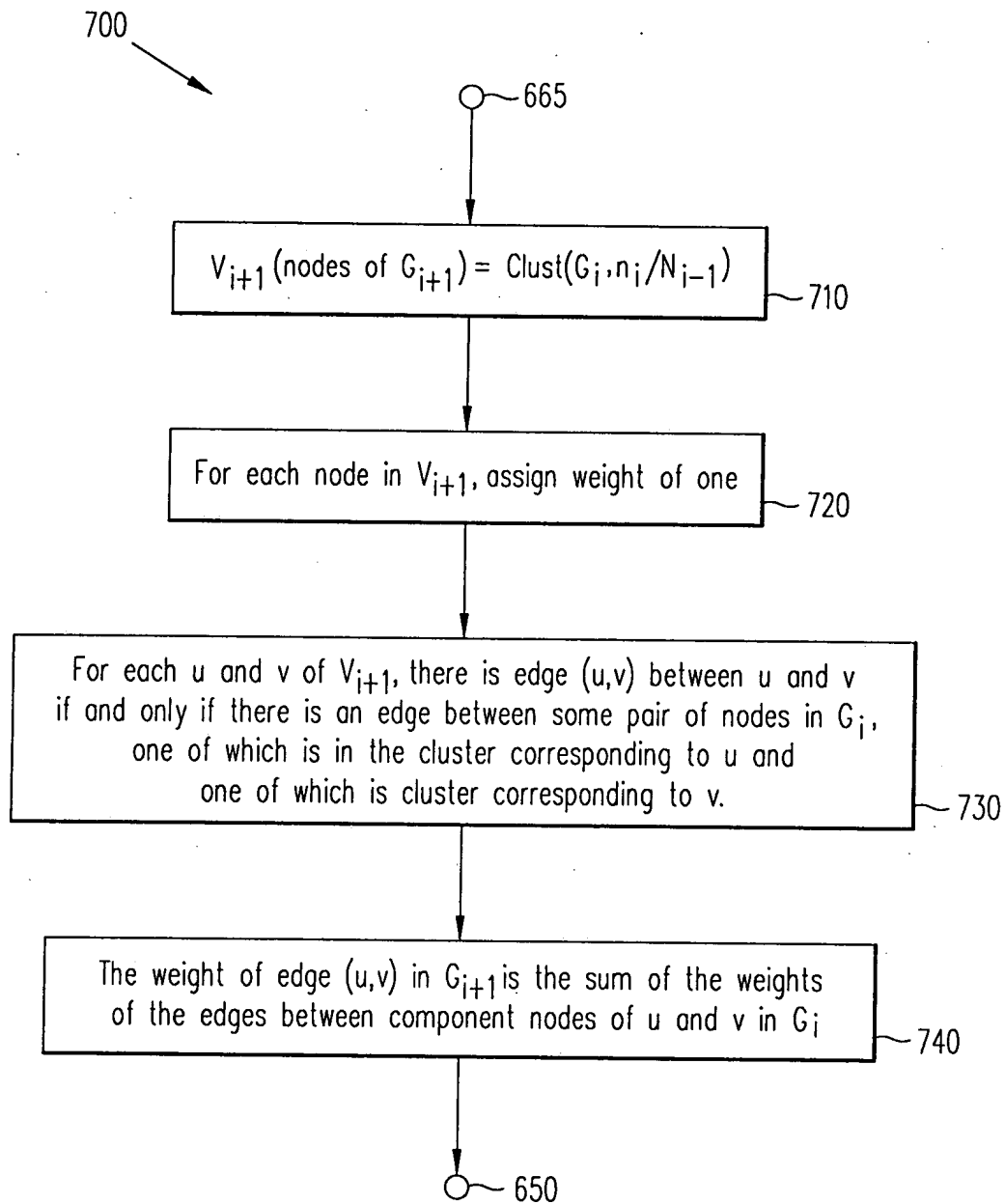


FIG. 7